



ITC 29 in GENOA

Ubiquitous, software-based, and sustainable networks and services

September 4-8, 2017

About ITC

The ITC conference 2017 will be held at the University of Genoa, Italy from September 4 to September 8. The 29th event of the series features the topic *Ubiquitous, software-based, and sustainable networks and services*.

Call for Papers

The ongoing “softwarization” process in networks, along with hardware capability and new services, is setting the pace for a tighter integration between computing and telecommunications technologies. At the same time, 5G and IoT are coming on the scene and will produce an unprecedented growth in wireless access and data generation. According to some estimates, there might be 25 billion connected things by 2020. The exponential growth of IoT nodes, flexibility in service provisioning, and programmability, are making networks more complex to manage and operate. In addition, network design presents new challenges, raised by ultra-low power consumption requirements of IoT nodes and by the use of less energy-efficient general-purpose hardware. The new paradigms will affect and shape the statistical features of teletraffic, along with its performance analysis and control.

ITC 29 aims at exploring some of the most challenging aspects in this teletraffic scenario, and is organized into 5 areas, which will cover a broad range of relevant aspects. Additionally, a Demo Session will be organized.

Area 1: Networking, Cloud/Fog/Ubiquitous Computing, and Applications (incl. energy, multimedia, security, privacy)

Co-Chairs: Steve Uhlig (QMUL, UK)
Cedric Westphal (UCSC, USA)

Area 2: Future Internet Architectures (incl. SDN, NFV, HPC, ICN, CCN)

Co-Chairs: Marco Canini (KAUST, KSA)
KK Ramakrishnan (UC Riverside, USA)

Area 3: Wireless Networks and Applications (incl. 5G, IoT)

Co-Chairs: Antonio Capone (Politecnico di Milano, Italy)
Jianwei Huang (CUHK, HK)

Area 4: Measurements and Big Data Analytics (incl. characterization, monitoring, debugging, fault-tolerance, reliability, forensics, simulation, QoE)

Co-Chairs: Pedro Casas (AIT, Austria)
Dario Rossi (Telecom ParisTech, France)

Area 5: Performance Evaluation, Control, and Optimization (incl. queueing, game theory, machine learning)

Co-Chairs: Nicolas Gast (INRIA, France)
Zhi-Li Zhang (University of Minnesota, USA)

Demo Session

Co-Chairs: Artur Hecker (Huawei, Germany)
Rick McGeer (SAP Americas Labs, USA)
Chuan Wu (Hong Kong University, HK)

Conference Co-Chairs

Franco Davoli, University of Genoa, Italy
Nikhil Jain, Qualcomm Technologies, USA

TPC Co-Chairs

Raffaele Bolla, University of Genoa, Italy
Florin Ciucu, University of Warwick, UK

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Matteo Repetto, CNIT, Italy

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Vice-Chair: Dario Rossi, Telecom ParisTech / Ecole Polytechnique, France

Important dates

Paper registration: ~~February 19~~ **April 5, 2017**
Full paper due: ~~March 05~~ **April 5**
Acceptance notification: May 24
Final paper due: June 18

Venue

Department of Architecture, Polytechnic School of Engineering and Architecture, University of Genoa

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